OIPE IAP74
OCT 10 2006
PATENT & TRADEMARK OFFICE

100A

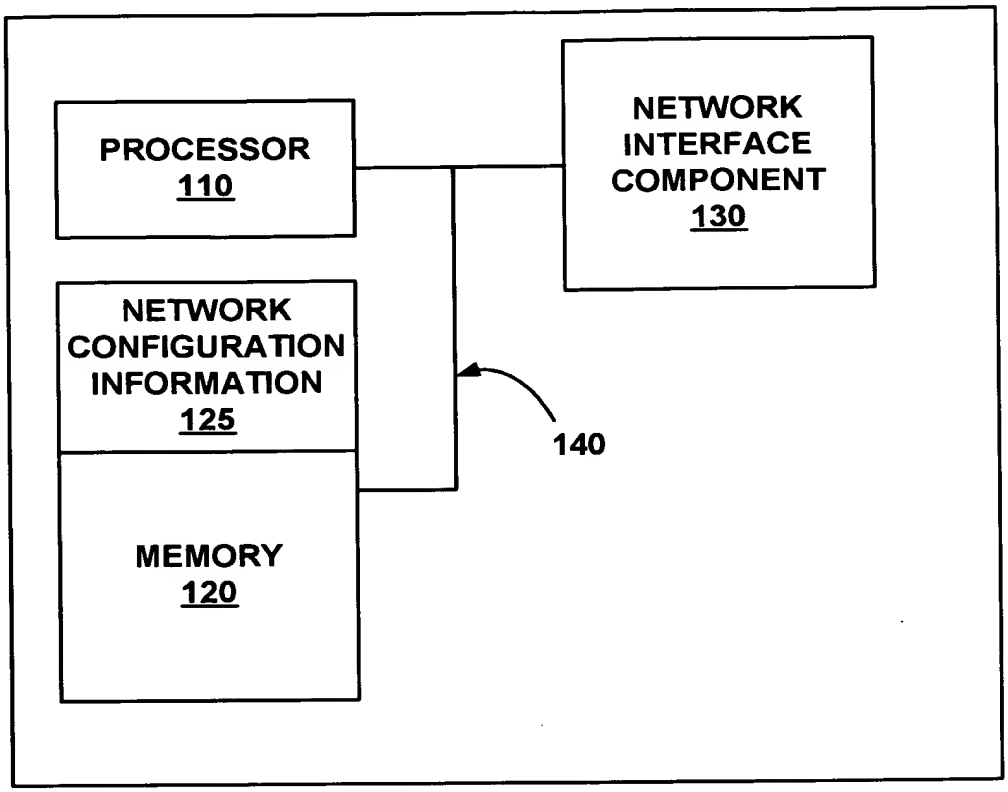

FIGURE 1A

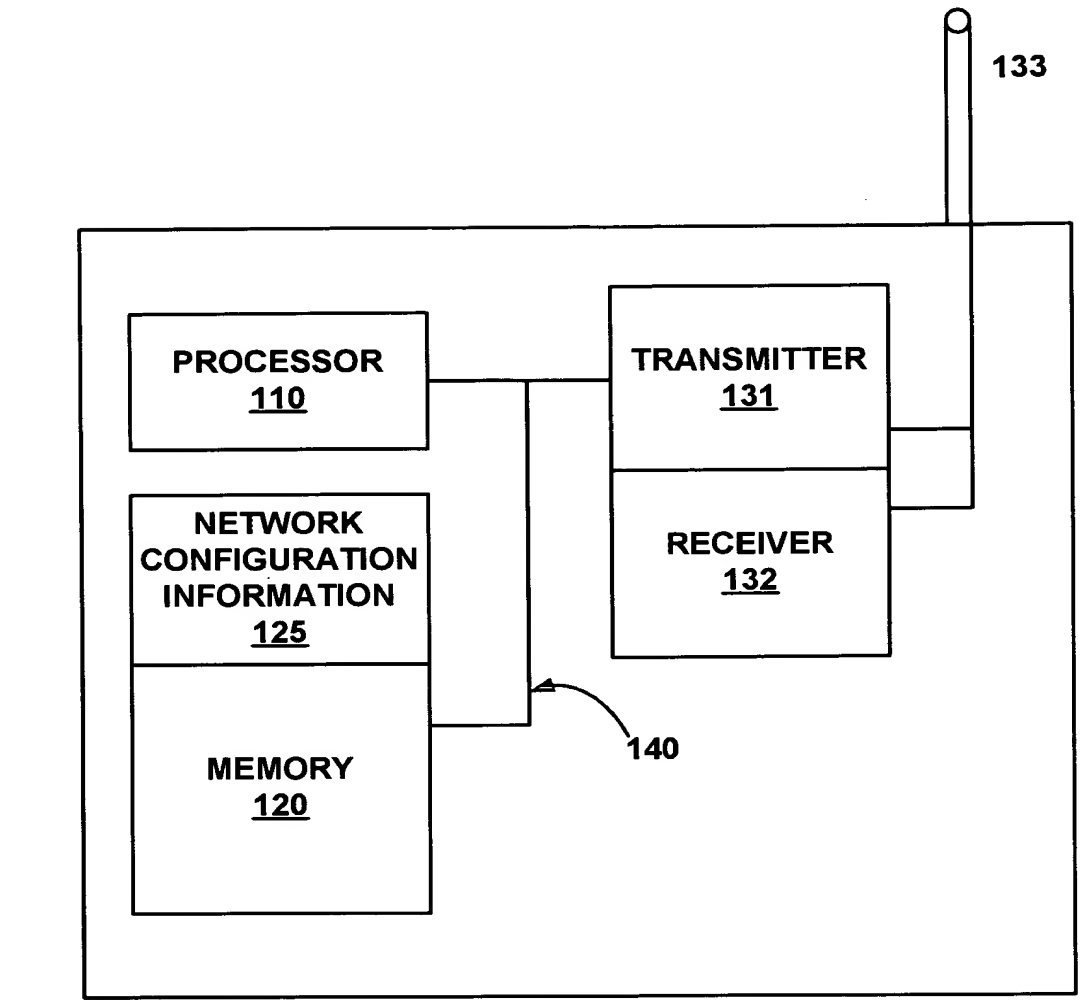
100B
133
PROCESSOR
110
TRANSMITTER
131
RECEIVER
132
NETWORK
CONFIGURATION
INFORMATION
125
MEMORY
120
140

FIGURE 1B

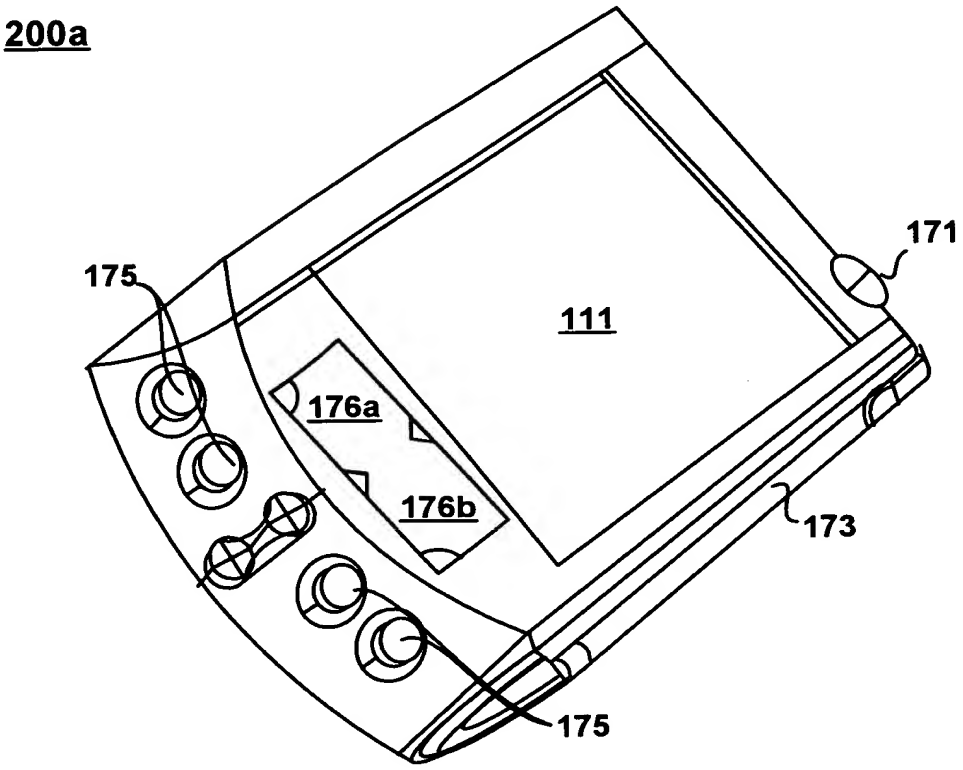

FIGURE 2A

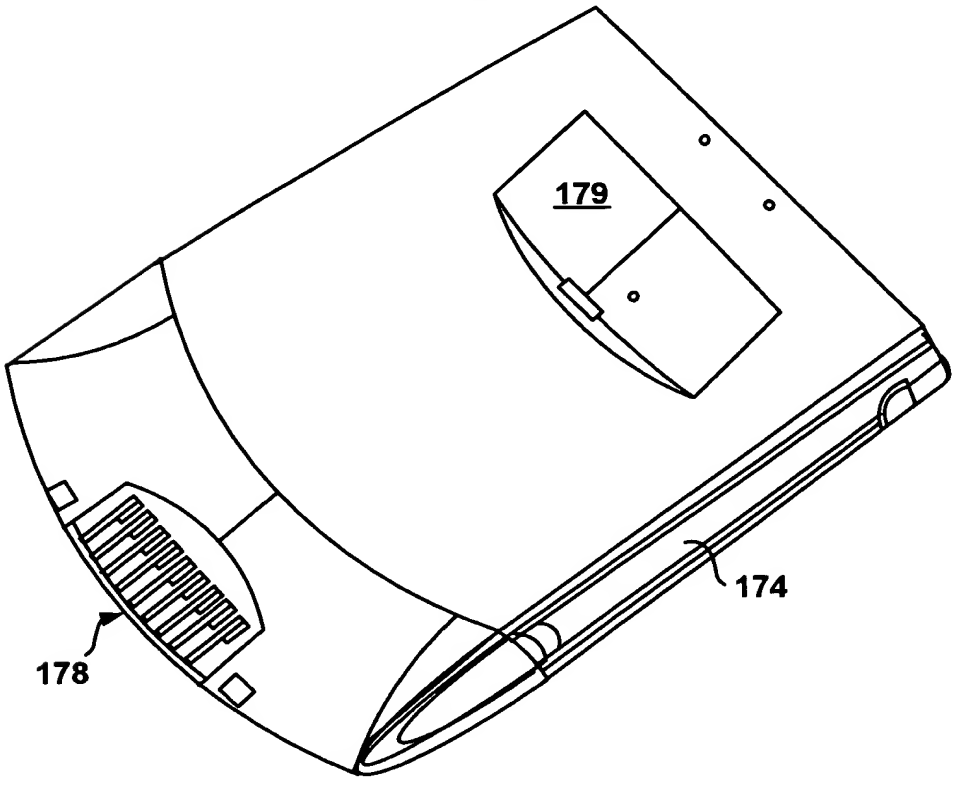

FIGURE 2B

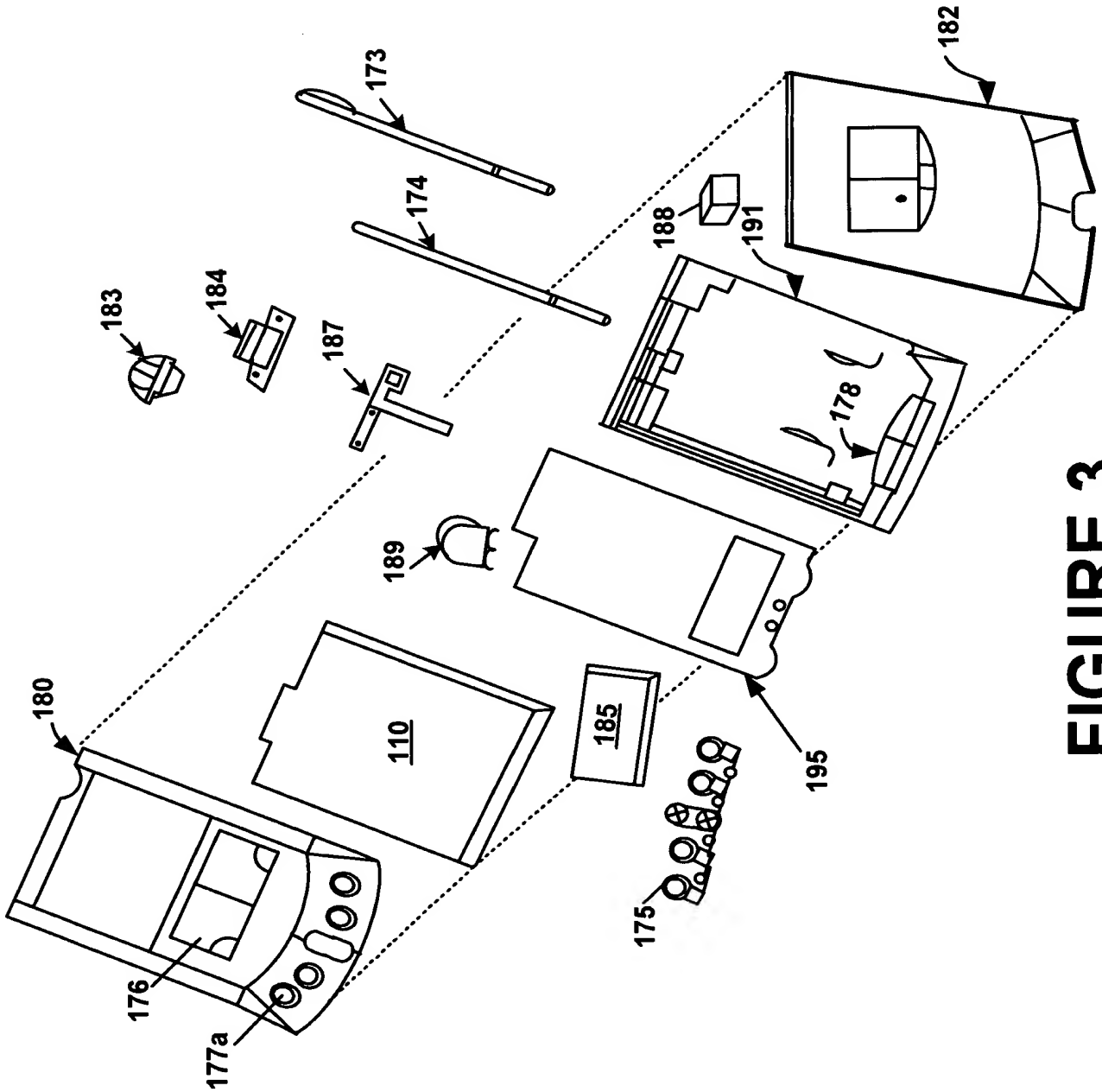
200
180
110
185
195
175
176
177a
189
183
184
187
173
174
188
191
182
178

FIGURE 3

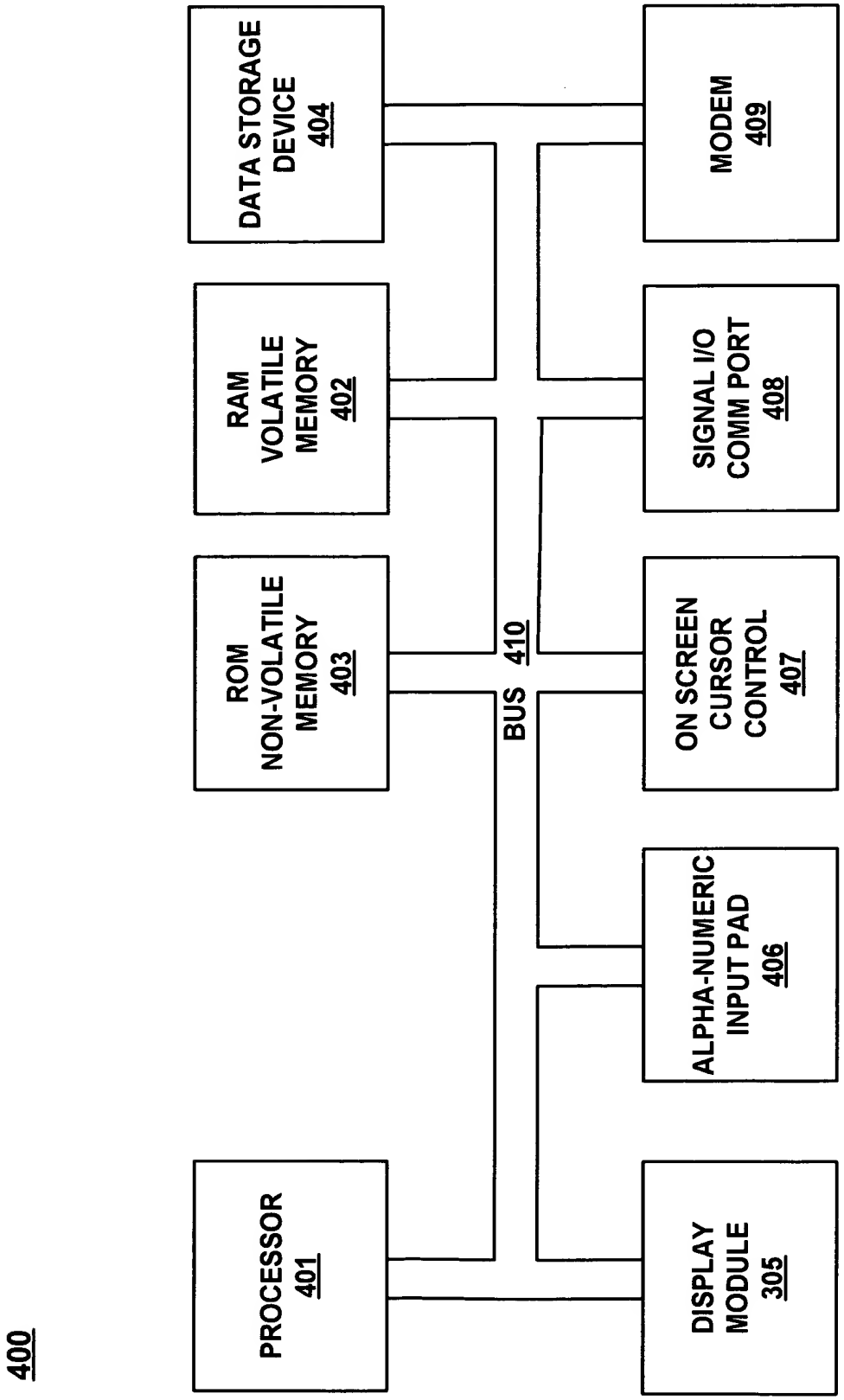
400
PROCESSOR 401
ROM NON-VOLATILE MEMORY 403
RAM VOLATILE MEMORY 402
DATA STORAGE DEVICE 404
BUS 410
DISPLAY MODULE 305
ALPHA-NUMERIC INPUT PAD 406
ON SCREEN CURSOR CONTROL 407
SIGNAL I/O COMM PORT 408
MODEM 409

FIGURE 4A

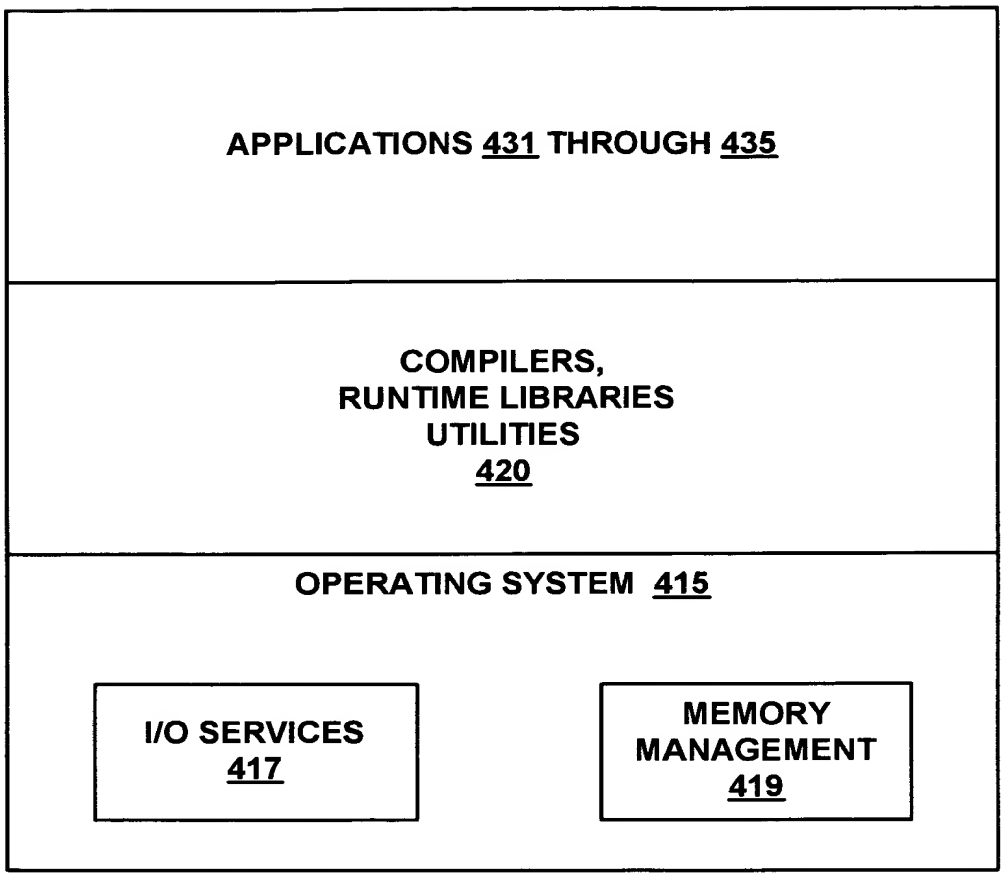
APPLICATIONS 431 THROUGH 435
COMPILERS,
RUNTIME LIBRARIES
UTILITIES
420
OPERATING SYSTEM 415
I/O SERVICES
417
MEMORY
MANAGEMENT
419

FIGURE 4B

500

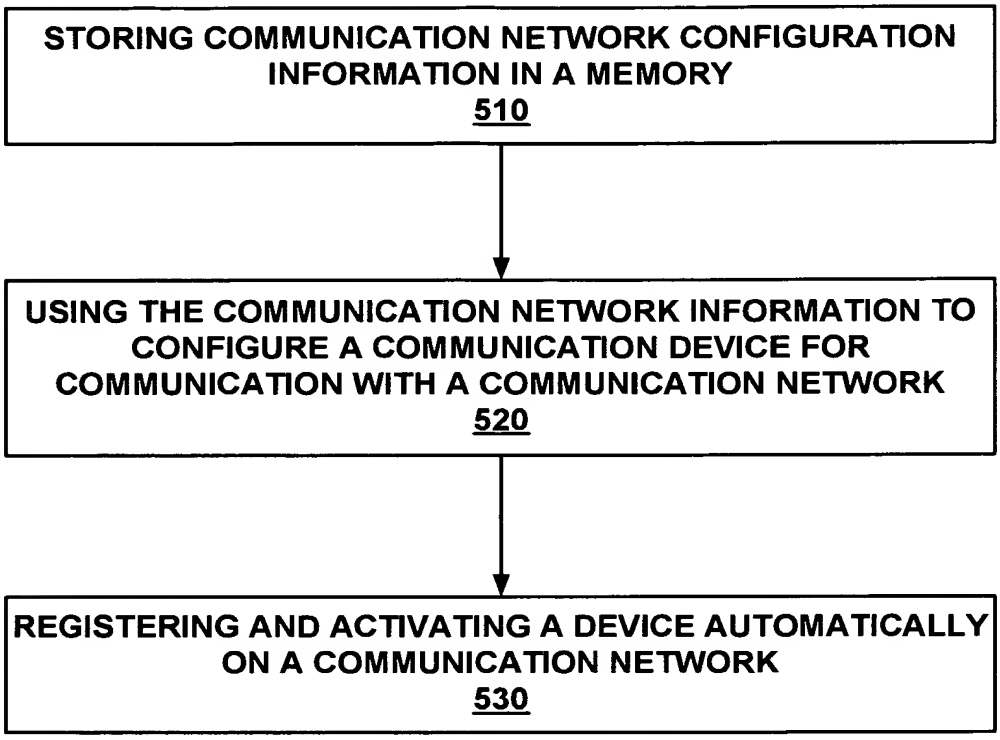
STORING COMMUNICATION NETWORK CONFIGURATION INFORMATION IN A MEMORY
510
USING THE COMMUNICATION NETWORK INFORMATION TO CONFIGURE A COMMUNICATION DEVICE FOR COMMUNICATION WITH A COMMUNICATION NETWORK
520
REGISTERING AND ACTIVATING A DEVICE AUTOMATICALLY ON A COMMUNICATION NETWORK
530

FIGURE 5